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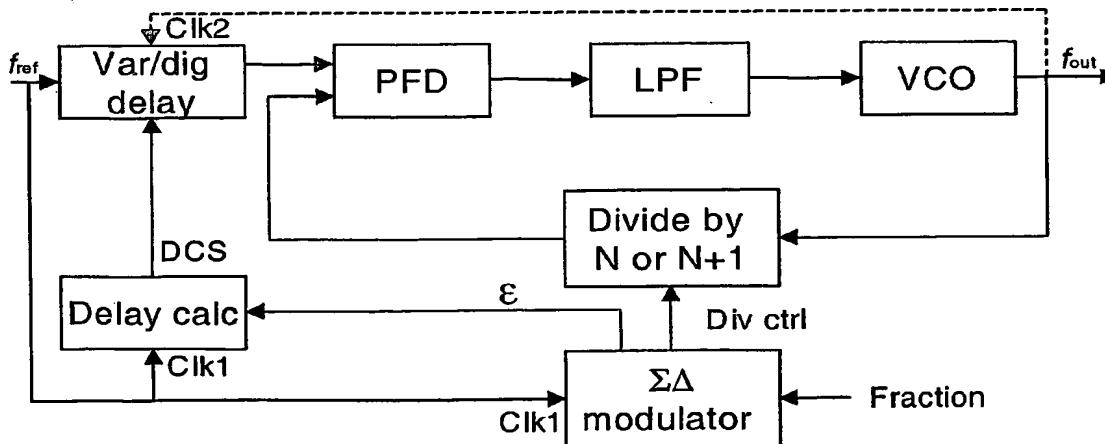
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(54) Title: METHOD AND SYSTEM OF JITTER COMPENSATION



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(57) Abstract: The present invention relates to sigma-delta modulators, $\Sigma\Delta$ modulators, and phase locked loops. Especially, it relates to jitter compensation in $\Sigma\Delta$ -controlled fractional-N frequency synthesizers. Jitter compensation is introduced by means of a variable delay line.



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